

## TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

<b>Title of Invention</b>	<b>FAST-DISABLED VOLTAGE REGULATOR CIRCUIT WITH LOW-NOISE FEEDBACK LOOP AND OPERATING METHOD THEREOF</b>				
<p>Application Number :</p> <p>Date :</p> <p>First Named Applicant:        Ling-Wei Ke</p> <p>Confirmation Number:</p> <p>Attorney Docket Number:        MTKP0080USA</p>					
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<table border="1"><tr><td>Submitted By:</td><td>Elec. Sign.</td></tr><tr><td>Winston Hsu Registered Number: 41,526</td><td>/Winston Hsu/</td></tr></table>		Submitted By:	Elec. Sign.	Winston Hsu Registered Number: 41,526	/Winston Hsu/
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Winston Hsu Registered Number: 41,526	/Winston Hsu/				

Documents being submitted:	Files
us-declaration	MTKP0080-0-A1-usdecl.xml us-declaration.dtd us-declaration.xsl
us-fee-sheet	MTKP0080-0-A1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-ids	MTKP0080-0-A1-usidst.xml us-ids.dtd us-ids.xsl
us-power-of-attorney-grant	MTKP0080-0-A1-uspoat.xml us-power-of-attorney-grant.dtd us-power-of-attorney-grant.xsl
us-assignment	MTKP0080-0-A1-usassn.xml us-assignment.xsl us-assignment.dtd MTKP0080-0-A1ASS1.tif MTKP0080-0-A1ASS2.tif
us-request	MTKP0080-0-A1-usrequ.xml us-request.dtd us-request.xsl
us-declaration	MTKP0080-0-A1DEC1.tif
us-declaration	MTKP0080-0-A1DEC2.tif
application-body	MTKP0080-0-A1-ABX.xml application-body.dtd Image1.tif Image2.tif Image3.tif Image4.tif Image5.tif isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isogr1.ent isogr2.ent isogr3.ent isogr4.ent isolat1.ent isolat2.ent isomfrk.ent isomopf.ent isomscr.ent isonum.ent

	isopub.ent
	isotech.ent
	mathml2.dtd
	mathml2-qname-1.mod
	mmlalias.ent
	mmlextra.ent
	soextblx.dtd
	us-application-body.xsl
	wipo.ent
application-body-pdf-wrap	MTKP0080-0-A1-ABX-pdf-wrap.xml
abstract-pdf	MTKP0080-0-A1-ABX-abst.pdf
claims-pdf	MTKP0080-0-A1-ABX-clms.pdf
description-pdf	MTKP0080-0-A1-ABX-desc.pdf
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